Novel Operational Amplifier based on a-InGaZnO TFTs for the Display Driving System

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Generally, complementary metal oxide semiconductor (CMOS) based operational amplifier (op-amp) has been the main part for the analog circuits due to its superior performance. In display applications, the op-amp is also usually adopted in the control blocks for the power supply system, sensor system, and analog-to-digital converter to the display driving systems. Among various TFT technologies developed so far in order to integrate the display driving system on TFT arrays and, therefore, to eliminate the IC assembly [1], amorphous Indium-Gallium-Zinc-Oxide thin-film transistors (a-InGaZnO TFTs) have shown favorable electrical characteristics, including processing temperature, low uniformity, decent mobility, low off-current, sharp sub-threshold swing, and a potentially better electrical stability, which are very favorable properties for use in integrated display driving circuitry [2]. In this study, we designed the a-InGaZnO TFT based op-amp that can be applied to various display driving system.

Fig. 1 shows the proposed circuit configuration. Different from silicon transistor based op-amp, the proposed op-amp is based on a-InGaZnO TFT and therefore, the entire circuit should be designed only with n-type transistors. The first block of op-amp consists of a fully differential-amp and source followers. The differential-amp amplifies difference between two input signals. In order to maintain a stable gain, it needs a stable tail current source which can be obtained by a current mirror circuit. Through source followers, differential-amp output signals are safely transferred to the next second block where two common-source amplifiers are combined together sequentially. The gain of each common-source amplifier can be obtained by the ratio between trans--conductances of input and load TFTs.

Fig. 2(a) shows the frequency response of the proposed op-amp based on a-InGaZnO TFTs. The simulation results indicate that the designed op-amp is expected to have an open-loop voltage gain of 29dB at up to 2.6kHz when the supply voltage is ± 15 V. As shown in Fig. 2(b), the proposed op-amp can be fabricated within the area of $1288.5 \mu m \times 943.5 \mu m$.

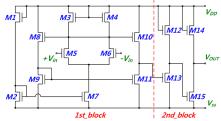
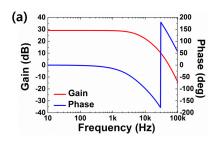


Fig. 1. Schematic of the proposed a-InGaZnO TFT based op-amp



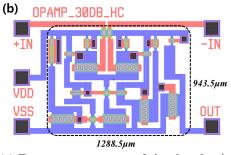


Fig. 2. (a) Frequency response of simulated gain, phase margin, and (b) layout of the proposed op-amp

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