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Exhibitor Introduction (Within 200 words)	<p>Silvaco, Inc. is a leading EDA provider of software tools used for process and device development and for analog/mixed-signal, power IC and memory design. Silvaco delivers a full TCAD-to-Sigoff flow for vertical markets including: displays, power electronics, optical devices, radiation & soft error reliability and advanced CMOS process and IP development. For over 30 years, Silvaco has enabled its customers to bring superior products to market with reduced cost and in the shortest time. The company is headquartered in Santa Clara, California, and has a global presence with offices located in North America, Europe, Japan and Asia.</p> <p>For over 30 years since the company's founding in 1984 by Dr. Ivan Pesic, semiconductor fabs and design houses from around the globe have relied on Silvaco's software expertise to help develop the "technology behind the chip", forming the transistors that power all consumer electronics. Silvaco's mission is to help our customers accelerate the pace of technological innovation and their time to market while reducing their costs in developing the next-generation chips that enable the digital revolution. We strive to understand our customers' challenges so as to tailor the innovative products, services and support they need to address these challenges and succeed in their technology development and productivity goals.</p>

<p>Exhibit Description (Within 200 words)</p>	<p>Silvaco's core products encompass six broad areas: 2D/3D TCAD, 3D RC Extraction, SPICE Model extraction, Custom Design, SPICE simulation and Power Integrity signoff. A sampling of the core products and their capabilities are described below:</p> <ul style="list-style-type: none"> • Power integrity signoff from block (analog, SRAM, custom digital) to full chip level including power, EM/IR and thermal analysis • Full custom design flow including schematic entry, layout, simulation and verification • Large portfolio of PDKs across many foundries with emphasis on AMS, HV, BCD and CIS processes • 16nm and 10nm FinFET ready Parallel SPICE simulator being extended to support FastSPICE applications • Variation-aware design tools comprising of Fast Monte Carlo, Local mismatch, statistical corners, high sigma analysis and statistical verification of standard cell libraries • Automated SRAM characterization environment • 3D parasitic RC extractor used for detailed and accurate FinFET SRAM extraction • SPICE modeling for large set of model types including HiSIM_HV for power devices and UOTFT for organic and oxide TFTs • Pixel and interconnect RC extraction for TFT displays <p>3D TCAD products used for large application space, including rapid FinFET prototyping, large structure parallelized simulations for multi-cell IGBTs, robust, stable oxidation simulation for trench MOS power devices and CMOS image sensors, high precision SiC/GaN simulation, advanced etch for 3D NAND Flash and STT MRAM, SEE and total dose reliability simulation</p>
<p>Exhibit Product</p>	<p>2D/3D TCAD</p> <ul style="list-style-type: none"> • Athena 2D Process • Victory 3D Process • Atlas 2D Device • Victory 3D Device • VWF DOE & Optimization <p>SPICE Simulation</p> <ul style="list-style-type: none"> • SmartSpice • SmartSpice RF

- SmartView
- Harmony AMS

Model Extraction

- UTMOST IV Device Characterization and SPICE Modeling
- Spayn Statistical Analysis
- TechModeler Fast Modeling

Variation Analysis

- Variation Manager
 - Fast MC
 - High Sigma
 - Library Verif

Power Integrity Signoff

- Invar
 - Power, IR
 - EM
 - Thermal

Custom Design

- Gateway Schematic Editor
- Expert Layout Editor
- Guardian DRC/LVS/NET
- Hipex Full Chip RCX

3D RCX

- Clever RC Extractor
- Quest L Extractor
- Exact LPE File Generator