

IMiD 2024

The 24th International Meeting on Information Display
August 20-23, 2024 / ICC Jeju, Jeju, Korea

Session Title:	59. Circuit Design and Implementations of TFT
Session Date:	August 23 (Fri.), 2024
Session Time:	13:10-14:25
Session Room:	Room C (Samda)

[C59-1] 13:10-13:25

Development of Reliability Diagnosis Tool of Oxide TFTs

Younghyun Ko, Gawon Yang, Chan-Yong Jeong, Jaeman Jang, Ju Heyuck Baeck, Won-Sang Ryu, Jiyong Noh, Kwon-Shik Park, and SooYoung Yoon (LG Display Co., Ltd., Korea)

[C59-2] 13:25-13:40

Split-Gate Organic Thin Film Transistor Compact Model For Logic Circuit Applications

Patryk Golec (Univ. of Surrey, UK), Seongjae Kim (Gachon Univ., Korea), Eva Bestelink (Univ. of Surrey, UK), Ahmed Mounir, Benjamin Iñiguez (Universitat Rovira i Virgili, Spain), Radu A. Sporea (Univ. of Surrey, UK), and Hocheon Yoo (Gachon Univ., Korea)

[C59-3] 13:40-13:55

A Compact 5T2C AMOLED Pixel Circuit with Multimodal LTPS Drive Transistor

Eva Bestelink and Radu A. Sporea (Univ. of Surrey, UK)

[C59-4] 13:55-14:10

Highly Efficient Neuromorphic Computing Devices with Charge Trap TFTs for Intelligent Displays

Hyunji Jeong, Seoungmin Park (Hanyang Univ., Korea), Dae-Hwan Ahn (KIST, Korea), and Younghyun Kim (Hanyang Univ., Korea)

[C59-5] 14:10-14:25

Possibility of Advanced BLDA of Lumped CW Scanning for Low-Cost LTPS Using WBC (Wavelength Beam Combiner) Technique

T. Noguch, T. Okada (Univ. of the Ryukyus, Japan), M. Hishida, N. Kobata, K. Miyano, and M. Nobuoka (Panasonic Connect Co., Ltd., Japan)